



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 2369

Bib Data Sheet

SERIAL NUMBER 10/751,463	FILING DATE 01/06/2004 RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 246841US-2 DIV
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Junichiro Noda, Kanagawa-ken, JAPAN;

Tamio Ikehashi, Kanagawa-ken, JAPAN;
Kenichi Imamiya, Tokyo, JAPAN;

** CONTINUING DATA *YES*

This application is a DIV of 10/270,673 10/16/2002 PAT 6,731,538
which is a CIP of 09/794,076 02/28/2001 ABN

** FOREIGN APPLICATIONS *NO*

JAPAN 2000-066954 03/10/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/09/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 20	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Phaniborn TP</i> Examiner's Signature Initials				

ADDRESS

22850
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.
1940 DUKE STREET
ALEXANDRIA, VA
22314

TITLE

Semiconductor memory device including page latch circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input checked="" type="checkbox"/> All Fees
RECEIVED	No. _____ for following:	<input checked="" type="checkbox"/> 1.16 Fees (Filing)
		<input checked="" type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input checked="" type="checkbox"/> 1.18 Fees (Issue)